

4th Trusted and Assured MicroElectronics (TAME) forum
“AI and Trusted and Assured Microelectronics
– Making both AI and Microelectronics more Secure”
May 9 and 10, co-located with HOST 2019

Co-chairs:

Rosario Cammarota, Intel AI Research
Brian Cohen, Institute of Defense Analysis

Thursday, May 9 2019

11:00 – 12:00 TAME registration for HOST attendees

12:00 – 13:15 Registration for TAME-only attendees

SESSION I

Session Chair: Waleed Khalil, OSU

13:15 – 13:30 **Introductory**

Co-Chairs: Rosario Cammarota, Intel AI Research, and Brian Cohen, IDA

13:30 – 14:15 **KEYNOTE I**

Speaker: Nicole Petta, Assistant Director for Microelectronics, Department of Defense, Office of the Undersecretary for Research & Engineering

14:15 – 15:00 **KEYNOTE II**

Speaker: Mukesh V. Khare, Vice President: Semiconductor and AI Hardware , IBM Research

15:00 – 15:30 **BREAK**

SESSION II

15:30 – 16:30 **VISIONARY TALK**

Speaker: Matthew Rosenquist, Cybersecurity Strategist, Intel AI

Breakout Session Reporting

16:30 – 17:30

Reception/Social Event

17:30 –19:00

Friday, May 10

7:00 – 8:00 Continental Breakfast

SESSION III

Chair: Domenic Forte, University of Florida

8:00 – 9:15 **Panel Discussion: Assured AI**

Panelists: TBD

9:15 – 9:45 **BREAK**

BREAKOUT/ WORKING GROUP PARALLEL SESSIONS

Chair: Rosario Cammarota, Intel AI Research

9:45 – 11:30

Session 1 Designed-in Security and Trust

Lead: Michel Kinsy, BU

Scribe: TBD

This group will discuss the grand challenges related to security and trust in the design phase with particular attention to how you can secure design processes against malicious action and what new design capabilities and architectures might be needed to support future secure systems and missions.

Session 2 National Hardware Vulnerability Database

Lead: Jeremy Bellay

Scribe: TBD

This group will discuss the motivation and benefit of having a national vulnerabilities database as well the challenges in creating such a database and what kind of plan of action might be needed to make it happen.

Session 3 National Technology Roadmap for Trusted and Assured Microelectronics

Lead: Domenic Forte

Scribe: TBD

This group will discuss the potential for creating a roadmap, leveraging the discussion in the previous panel. The discussion should consider the purpose for the roadmap, who would be the audience and develop recommendations on a plan of action for creating such a roadmap.

CLOSING REMARKS

11:30 – 12:00pm Joint session to update the group. Leads update on the working groups outcome.